

Application Data Sheet

Application Information

Application Type:: Regular
Subject Matter:: Utility
Title:: NONVOLATILE SEMICONDUCTOR MEMORY
HAVING PLURAL DATA STORAGE PORTIONS
FOR A BIT LINE CONNECTED TO MEMORY
CELLS
Attorney Docket Number:: 001701.00140
Request for Early Publication?:: NO
Request for Non-Publication?:: NO
Total Drawing Sheets:: 70
Small Entity?:: NO
Secrecy Order in Parent Appl.?:: NO

Applicant Information

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Status:: Full Capacity
Given Name:: Ken
Family Name:: Takeuchi
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Country of Residence:: Japan
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State or Province of mailing address::
Country of mailing address:: Japan
Postal or Zip Code of mailing address:: 105

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Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Status:: Full Capacity
Given Name:: Tomoharu
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Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Status:: Full Capacity
Given Name:: Noboru
Family Name:: Shibata
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Correspondence Information

Correspondence Customer Number:: 22907

Representative Information

Representative Customer Number:: 22907

Domestic Priority Information

→ now 6,373,746 B1

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Divisional	09/667,610	9/22/00

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	11-275327	9/28/99	Yes
Japan	11-345299	12/3/99	Yes

Assignee Information

Assignee name:: Kabushiki Kaisha Toshiba
City of mailing address:: Kawasaki-shi
Country of mailing address:: Japan